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Publication number:

0 322 615  
A1



## EUROPEAN PATENT APPLICATION

② Application number: 88120580.1

⑤ Int. Cl.: C30B 25/02, C30B 29/36,  
H01L 21/365

③ Date of filing: 09.12.88

The title of the invention has been amended  
(Guidelines for Examination in the EPO, A-III,  
7.3).

④ Priority: 19.12.87 JP 320100/87

④ Date of publication of application:  
05.07.89 Bulletin 89/27

④ Designated Contracting States:  
DE FR GB

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⑥ Method of growing a single crystalline beta-silicon carbide layer on a silicon substrate.

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⑥ A single crystalline silicon carbide ( $\beta$ -SiC) layer having a thickness greater than 1  $\mu\text{m}$  is grown on a silicon substrate by the following method of the present invention. The silicon substrate is provided in a reactor chamber, and the reactor chamber is evacuated and maintained at a reduced atmospheric pressure during the growing processes. While flowing a mixed gas containing acetylene into the reactor chamber, the substrate is heated up at a temperature range from 800 to 1000 °C, preferably in a range from 810 to 850 °C, whereby a buffer layer of carbonized silicon having a thickness of 60 to 100 Å is grown on the substrate. Thereafter, the flowing gas is changed to a mixed gas containing hydrocarbon and chlorosilane, and the substrate temperature is raised to a temperature from 850 to 950 °C. In this process, a single crystalline  $\beta$ -SiC layer can be grown on the buffer layer, and a thickness of a few

$\mu\text{m}$  for the grown  $\beta$ -SiC layer can be expected.

## A METHOD OF GROWING A SINGLE CRYSTALLINE $\beta$ -SiC LAYER ON A SILICON SUBSTRATE

### BACKGROUND OF THE INVENTION

#### 1. Field of the Invention

This invention relates to a method of growing a  $\beta$ -SiC layer on a silicon substrate. More specifically, this invention concerns a method of growing a single crystalline  $\beta$ -SiC layer on a silicon substrate, in which all processes are performed heating the substrate at temperatures below 1000 °C. A heterojunction formed between a  $\beta$ -SiC layer and a silicon substrate has a promising application for high-speed and high-power transistors working at a high temperature.

#### 2. Description of the Related Art

Compared with silicon, silicon carbide SiC has a wide energy gap of 2.2 eV and has other distinctive features in heat resisting property, thermal conductivity, break down voltage, saturated electron drift velocity, etc. Silicon carbide has several crystalline structures such as 3C-SiC (cubic system, called  $\beta$ -SiC) and 6H-SiC (hexagonal system, called  $\alpha$ -SiC). Especially,  $\beta$ -SiC having an energy gap of 2.2 eV is popular and has been the subject of investigation for developing a high temperature FET (Field Effect Transistor), a light emitting diode of green rays, and a heterojunction bipolar transistor using the heterojunction between a silicon substrate and a single crystalline  $\beta$ -SiC layer grown thereon.

In order to grow a single crystalline  $\beta$ -SiC layer on a silicon substrate, several methods have been investigated. Among them, a CVD method is most popular, in which the substrate is heated up at a temperature around 1300 °C using, for example, propane  $C_3H_8$  and silane  $SiH_4$  (or trichlorosilane  $SiHCl_3$ ) gases as source material gases and a hydrogen gas as a carrier gas.

The problem encountered in the above method is that the grown  $\beta$ -SiC layer shows deterioration in a crystalline structure when a thickness of the grown  $\beta$ -SiC layer becomes greater than 1  $\mu m$ . The grown layer does not show a  $\beta$ -SiC epitaxial layer or a single crystalline structure of  $\beta$ -SiC but rather a poly-crystalline structure. Therefore, it can not be used for fabricating a semiconductor device using a thick  $\beta$ -SiC layer on a silicon substrate. The main reason for the deterioration of crystalline structure is due to a lattice mismatch between the

silicon and  $\beta$ -SiC crystals. Because the lattice constant of silicon is 5.430 Å and that of  $\beta$ -SiC 4.358 Å, the difference being about 20 % therebetween.

In order to improve the crystalline quality of the grown  $\beta$ -SiC layer, a method of growing an intermediate layer by carbonizing silicon substrate between the silicon substrate and the  $\beta$ -SiC layer has been proposed. The intermediate layer is comparatively thin and relieves the stress between the silicon substrate and the grown single crystalline  $\beta$ -SiC layer. The intermediate layer having a thickness of several tens Å to a few hundred Å acts as a buffer layer for the epitaxial  $\beta$ -SiC layer.

The method of growing the buffer layer of the prior art includes the steps of heating up the silicon substrate in a temperature range between 1300 °C to 1350 °C, and introducing a propane  $C_3H_8$  gas as a source material gas. When a thickness of the buffer layer is sufficiently thin, the grown  $\beta$ -SiC layer on the buffer layer shows a single crystalline  $\beta$ -SiC structure and it does not become a serious problem in forming a heterojunction between the silicon substrate and the  $\beta$ -SiC epitaxial layer grown on the buffer layer. Though it has become possible to decrease the growth temperature of the single crystalline  $\beta$ -SiC on the buffer layer to a temperature less than 1000 °C, the temperature 1300 to 1350 °C for growing the buffer layer is extremely high when the fact is taken into consideration that the melting point of silicon is about 1400 °C.

The high temperature of growing the buffer layer beneath the single crystalline  $\beta$ -SiC layer in the prior art has bad influences on the silicon substrate such as extension of an impurity diffusion region, mechanical deformation of the substrate, etc. Many efforts have been directed to decrease the growth temperature of the buffer layer or to eliminate the process of growing the buffer layer by growing the  $\beta$ -SiC epitaxial layer directly on the silicon substrate.

### SUMMARY OF THE INVENTION

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It is a general object of the invention, therefore to provide an improved method of growing a single crystalline  $\beta$ -SiC epitaxial layer above a silicon substrate sandwiching an intermediate buffer layer.

It is a more specific object of the invention to provide a method in which all processes are performed in a temperature range lower than 1000 °C.

It is another object of the invention to provide a method of growing an intermediate buffer layer and

a single crystalline  $\beta$ -SiC layer continuously on a silicon substrate utilizing a simple system of chemical vapor deposition (abbreviated as CVD), wherein the source material gases are changed and a temperature of the substrate is controlled lower than 1000 °C during all growing processes.

It is still another object of the invention to provide a method of growing a single crystalline  $\beta$ -SiC layer having a superior quality in surface morphology of the grown  $\beta$ -SiC epitaxial layer, even when a thickness thereof exceeds a few  $\mu\text{m}$ .

These and other objects are accomplished by a method using a CVD process of the present invention, in which an intermediate buffer layer of such as carbonized silicon is grown on a silicon substrate using an acetylene  $\text{C}_2\text{H}_2$  gas as a source material gas and heating the substrate up to a temperature range between 800 and 1000 °C, preferable between 810 to 850 °C. Thereafter a  $\beta$ -SiC epitaxial layer is grown thereon using the mixed gas of a chlorosilane (trichlorosilane  $\text{SiHCl}_3$  or dichlorosilane  $\text{SiH}_2\text{Cl}_2$ ) gas and a hydrocarbon (acetylene  $\text{C}_2\text{H}_2$  or propane  $\text{C}_3\text{H}_8$ ) gas as source material gases.

Because an acetylene  $\text{C}_2\text{H}_2$  gas is more active than other hydrocarbon gases, it is expected to react with silicon at temperatures below 1300 °C. In the present invention, it is found that the buffer layer can be grown at a temperature as low as 800 °C when an acetylene gas  $\text{C}_2\text{H}_2$  is used as a source material gas.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a schematic partial cross section of a silicon substrate together with a buffer layer and a single crystalline  $\beta$ -SiC layer according to the present invention.

Fig. 2 is a gas flow diagram and a schematic cross section of a reactor chamber for chemical vapor deposition utilized in the present invention.

Fig. 3(a) shows a surface roughness of the grown single crystalline  $\beta$ -SiC layer by the present invention measured by a surface profiler ALPHA-STEP by TENCOR INSTRUMENTS, and Fig. 3(b) shows the similar curve for the grown  $\beta$ -SiC epitaxial layer by the prior art method.

Fig. 4(a) shows an X-ray diffraction pattern for the  $\beta$ -SiC epitaxial layer grown by the present invention, and Fig. 4(b) shows the similar pattern for the  $\beta$ -SiC epitaxial layer grown by the prior art method, and

Fig. 5 shows an example of a growth rate characteristic of the single crystalline  $\beta$ -SiC versus substrate temperature.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Fig. 1 is a schematic partial cross section of a silicon substrate 1 together with two grown layers thereon according to the present invention. The lower layer is a buffer layer 2 of carbonized silicon and the upper layer is a single crystalline  $\beta$ -SiC layer 3. These two layers are grown on the silicon substrate 1 one after another by a CVD method. In growing a single crystalline  $\beta$ -SiC epitaxial layer, it is preferable to use a silicon substrate with an off-axial (111) orientation. In the embodiments, a silicon substrate with an orientation of 4°-off (111) toward [2  $\bar{1}\bar{1}$ ] (hereinafter, abbreviated as Si(111) 4°-off) is utilized.

The reason for using a silicon (111) substrate with an off-axial orientation is disclosed in the following report and Japanese Unexamined Patent Publication, and their disclosures are hereby incorporated by reference.

"Heteroepitaxial  $\beta$ -SiC on Si" by Y. Furumura et al: Journal of the Electrochemical Society, Vol. 135, No. 5, May 1988.

"SHO-62-155512" opened on July 10, 1987 by M. Doki, F. Mieno, et al.

The above two disclosures are related to a method of growing a single crystalline  $\beta$ -SiC layer directly on a silicon (111) substrate with an off-axial orientation without a buffer layer, in which the single crystalline  $\beta$ -SiC layer is grown at a temperature equal to or lower than 1000 °C. The first reference discloses that a single crystalline  $\beta$ -SiC layer having a thickness of a few hundred nm is satisfactorily grown on the silicon substrate. The angle between 0.5° and 10° for the off-axial orientation can be utilized. However, in the following embodiments, a Si(111) 4°-off substrate is utilized.

When a single crystalline  $\beta$ -SiC layer having a thickness greater than 1  $\mu\text{m}$  is needed for fabricating a heterojunction bipolar transistor or a high temperature FET, it is found that the surface morphology of the  $\beta$ -SiC layer grown by the above referred method deteriorates with an increase of the thickness. Nevertheless, it is still effective to utilize a Si(111) 4°-off substrate for a case where the buffer layer is formed between the silicon substrate and the  $\beta$ -SiC epitaxial layer.

In the prior methods of growing a buffer layer with the aim of relaxing a lattice mismatch between a silicon substrate and a  $\beta$ -SiC epitaxial layer, a high temperature of above 1300 °C is needed for growing the buffer layer as explained in the related art. In order to decrease the temperature of growing the buffer layer, the method of the present invention utilizes an apparatus shown in Fig. 2. It shows a gas flow diagram and a schematic cross

section of a reactor chamber for chemical vapor deposition.

In Fig. 2, the silicon substrate 1 is mounted on a susceptor 12 and installed in a reactor chamber 10 of a horizontal type. Usually, a plurality of substrates is arranged in the reactor chamber 10 and processed at the same time. Outside the reactor chamber 10, a high frequency induction coil 14 is provided to heat up the susceptor 12 of carbon such as graphite together with the substrate 1 mounted thereon. The reactor chamber 10 provides an inlet 16, through which a mixed gas is introduced from a gas supply system, and further provides an outlet 18, with which an exhaust system 20 is connected.

A gas supply system in Fig. 2 comprises three material gas sources. An acetylene  $C_2H_2$  gas source 22 is provided in a first bottle; a propane  $C_3H_8$  or acetylene gas source 24, in second bottle; and a trichlorosilane  $SiHCl_3$  or dichlorosilane  $SiH_2Cl_2$  gas source 26, in a bubbler. Further, a hydrogen  $H_2$  gas source 28 is provided in a third bottle, and the hydrogen gas is used as a carrier gas, which dilutes the material gas and removes the native oxide layer on the substrate 1. Because trichlorosilane  $SiHCl_3$  or dichlorosilane  $SiH_2Cl_2$  is liquid at room temperature, it is bubbled by a hydrogen gas which is supplied from the hydrogen gas source 28.

Each gas source comprises a mass flow meter MFM and a flow control valve  $V_c$  such as a needle valve. A combination of the mass flow meter MFM and flow control valve  $V_c$  may be replaced by a mass flow controller. As for the bubbler, a vaporization controller 30 is provided, which includes plural valves and controls a flow rate of a hydrogen bubbling gas and an output mixed gas of hydrogen and trichlorosilane  $SiHCl_3$  (or dichlorosilane  $SiH_2Cl_2$ ).

After loading the silicon substrates 1 in the reactor chamber 10, the exhaust system 20 is used to evacuate the reactor chamber 10. The pressure in the reactor chamber is reduced, and is maintained at a pressure less than 10 Torr (typically around 3 to 5 Torr). This pressure value is maintained during the whole subsequent growing processes. The main valve  $V_m$  and the flow control valve  $V_{c1}$  for the hydrogen gas source 26 are opened. The flow control valve  $V_{c2}$  is adjusted such that a flow rate of the hydrogen gas such as 1 to 20 SLM (Standard Liter per Minute) can be obtained. In the embodiment, a gas flow rate in a range from 7 to 10 SLM is used. Thereafter, the susceptor 12 is heated up for about 10 minutes by the high frequency induction coil 14, resulting in raising a substrate temperature up to 800 to 1000 °C. A lower temperature range such as 810 to 850 °C is preferable. This process cleans the surface

of the silicon substrate 1 by removing a thin film of silicon dioxide formed by natural oxidation.

Maintaining the hydrogen gas flow and the substrate temperature untouched, the flow control valve  $V_{c1}$  for the acetylene  $C_2H_2$  gas source 22 is opened, and the flow rate of the acetylene gas is controlled in a range from 5 to 30 SCCM (Standard Cubic Centimeter per Minute), preferable in a range from 15 to 25 SCCM. With an introduction of the acetylene gas, a silicon substrate is carbonized and a buffer layer begins to grow on the silicon substrate 1. The buffer layer having a thickness of 6 to 10 nm can be obtained for 5 to 10 minute growth. The growth rate of the buffer layer depends on the substrate temperature and the flow rate of the acetylene gas.

And next, the flow control valve  $V_{c1}$  is closed, and the flow control valves  $V_{c2}$  and  $V_{c3}$  are opened. The flow rate of a propane  $C_3H_8$  gas is controlled in a range between 10 and 40 SCCM, and the flow rate of a trichlorosilane  $SiHCl_3$  gas is controlled at about 0.1 to 1.0 SLM maintaining the same flow rate for the hydrogen gas source 28. A mixing ratio of the propane gas to trichlorosilane gas is preferable to be about 1 : 30-35. Because the hydrogen gas is a carrier gas, its flow rate is much larger than that of the above two source gases added together, for example, about 10 times of the flow rate of the mixture of other two gases. While introducing a mixed gas of propane  $C_3H_8$ , trichlorosilane  $SiHCl_3$  and hydrogen into the reactor chamber 10, the substrate 1 is heated up to a temperature about or less than 1000 °C. A temperature range of  $900 \pm 50$  °C is more preferable. During the above growing process, a single crystalline  $\beta$ -SiC layer 3 is grown on the buffer layer 2 as shown in Fig. 1. When a lower growth temperature is applied during the growth, the growth rate of the  $\beta$ -SiC epitaxial layer becomes smaller. An example of the growth rate characteristic versus substrate temperature is shown in Fig. However, the lower substrate temperature during the growth is desirable to avoid bad influences on the substrate structure.

In the above embodiment, the trichlorosilane  $SiHCl_3$  gas source may be replaced by a dichlorosilane  $SiH_2Cl_2$  gas source. And further, the propane  $C_3H_8$  gas can be replaced by other hydrocarbon gases having a molecular structure comprising two or three carbon atoms in a single molecule, such as an acetylene  $C_2H_2$  gas. In case of using an acetylene gas, the acetylene  $C_2H_2$  gas source 22 can be used as a common gas source for growing both buffer layer 2 and single crystalline  $\beta$ -SiC layer 3.

Fig. 3(a) shows a trace of a measured surface roughness of the grown  $\beta$ -SiC layer on a Si(111) 4°-off substrate according to the method of the

present invention, and it is clear that the surface of the  $\beta$ -SiC grown by the present invention is very flat. In the figure, the ordinate of the curve indicates a measured relative height of the grown surface detected by a stylus, whereby the stylus is scanned horizontally on the substrate surface. The curve is measured using a surface profiler called ALPHA-STEP made by TENCOR INSTRUMENTS, Calif. U.S.A. Test pieces are prepared from 2.7  $\mu\text{m}$  thick single crystalline  $\beta$ -SiC grown on a Si(111) 4°-off substrate with a buffer layer of 60 Å thickness. The growth temperature of the buffer layer is about 850 °C and that for the single crystalline  $\beta$ -SiC layer is about 900 °C, thereby the trichlorosilane SiHCl<sub>3</sub>, propane C<sub>3</sub>H<sub>8</sub> and hydrogen gas system being used. Fig. 3(a) shows that even the maximum deviation from an average level falls within about  $\pm 50$  Å.

The similar curve for a single crystalline  $\beta$ -SiC grown directly on a Si(111) 4°-off substrate by the prior art method referred previously is shown in Fig. 3(b). In this case, the  $\beta$ -SiC layer is grown with a thickness of about 2.5  $\mu\text{m}$ . The figure shows peak deviations from an average level exceed over  $\pm 40$  nm levels.

Fig. 4(a) shows an X-ray diffraction pattern using a Cu-K $\alpha$  radiation for the grown  $\beta$ -SiC epitaxial layer on a Si(111) 4°-off substrate according to the present invention. And Fig. 4(b) shows the similar pattern for the  $\beta$ -SiC layer, which is grown directly on a silicon substrate without the buffer layer by the prior art method.

In Fig. 4(a), only two peaks, a strong (111)  $\beta$ -SiC peak and a relatively weak (222)  $\beta$ -SiC peak, are observed. Because the latter index (222) is an integral multiple of the index (111), it shows that the grown  $\beta$ -SiC layer of Fig. 4(a) is composed of a good single crystal. On the other hand, two extra peaks, i.e., (220) and (311)  $\beta$ -SiC peaks are observed in Fig. 4(b). It indicates that the grown  $\beta$ -SiC layer by the prior art method deteriorates in a crystalline structure and the grown layer is polycrystallized.

The results show that the method of the present invention makes it possible to grow a single crystalline  $\beta$ -SiC layer having a thickness greater than 1  $\mu\text{m}$  at a low temperature less than 1000 °C. The growth temperatures are 800 to 1000 °C (preferable 810 to 850 °C) for the buffer layer. The growth temperature for the single crystalline  $\beta$ -SiC layer is around or lower than 1000 °C (preferable 900  $\pm 50$  °C).

Compared with silicon,  $\beta$ -SiC has a wide energy gap, higher saturation electron velocity, higher break down voltage, and stronger mechanical strength. A thick single crystalline  $\beta$ -SiC layer on a silicon substrate will have many applications in a semiconductor field. Semiconductor devices with

high quality, such as heterojunction diodes, heterojunction bipolar transistors and FETs, can be expected applying the method of the present invention.

5 The present invention may be embodied in other specific forms without departing from the spirit or essential characteristics thereof. The presently disclosed embodiments are therefore to be considered in all respects as illustrative and not restrictive, the scope of the invention being indicated by the appended claims, rather than the forgoing description, and all changes which come within the meaning and range of equivalence of the claims are, therefore, to be embraced therein.

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### Claims

- 20 1. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate, said method comprising the following steps of:
  - (a) providing said silicon substrate in a reactor chamber,
  - (b) reducing a pressure inside said reactor chamber to a subatmospheric level,
  - (c) flowing a first mixed gas containing acetylene into said reactor chamber,
  - (d) heating said silicon substrate at a temperature between 800 and 1000 °C, whereby a buffer layer of carbonized silicon is grown on said silicon substrate,
  - (e) stopping said first mixed gas and flowing a second mixed gas containing hydrocarbon and chlorosilane gases into said reactor chamber, and
  - (f) heating said silicon substrate at a temperature lower than 1000 °C, whereby said single crystalline silicon carbide ( $\beta$ -SiC) layer is grown on said buffer layer.
- 30 2. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate according to claim 1, wherein said hydrocarbon gas in step (e) is selected from a group of propane and acetylene gases.
- 35 3. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate according to claim 1, wherein said chlorosilane gas in step (e) is selected from a group of trichlorosilane and dichlorosilane gases.
- 40 4. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate according to claim 1, wherein said silicon substrate has a surface orientation of 0.5° to 10° off (111) toward [2 1 1].
- 45 5. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate according to claim 1, wherein said step (d) of

growing said buffer layer continues until said buffer layer having a thickness from 60 to 100 angstroms is obtained.

6. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate according to claim 1, wherein said temperature of the silicon substrate in step (d) is in a range from 810 to 850 °C.

7. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate according to claim 1, wherein said temperature of the silicon substrate in step (f) is in a range between 850 to 950 °C.

8. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate according to claim 1, wherein said first mixed gas in step (c) contains a hydrogen gas and a mixing ratio of said hydrogen gas to acetylene gas is greater than 10.

9. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate according to claim 1, wherein a mixing ratio of said chlorosilane gas to hydrocarbon gas in step (e) is greater than 30.

10. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate according to claim 1, wherein said second mixed gas in step (e) contains a hydrogen gas and a mixing ratio of said hydrogen gas to other gases put together is greater than 10.

11. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate according to claim 1, wherein the growing process of said single crystalline silicon carbide ( $\beta$ -SiC) layer in steps (e) and (f) continues until said  $\beta$ -SiC layer having a thickness greater than 1  $\mu\text{m}$  is obtained.

12. A method of growing a single crystalline silicon carbide ( $\beta$ -SiC) layer on a silicon substrate, said method comprising the following steps of:

(a) providing said silicon substrate in a reactor chamber.

(b) reducing a pressure inside said reactor chamber to a subatmospheric level.

(c) flow a hydrogen gas into said reactor chamber,

(d) heating said silicon substrate at a temperature between 800 and 1000 °C, whereby a surface of said silicon substrate is cleaned,

(e) flowing a first mixed gas containing acetylene into said reactor chamber,

(f) heating said silicon substrate at a temperature between 800 and 1000 °C, whereby a buffer layer of carbonized silicon is grown on said silicon substrate,

(g) stopping said first mixed gas and flowing a second mixed gas containing hydrocarbon and chlorosilane gases into said reactor chamber, and

5 (h) heating said silicon substrate at a temperature lower than 1000 °C, whereby said single crystalline silicon carbide ( $\beta$ -SiC) layer is grown on said buffer layer.

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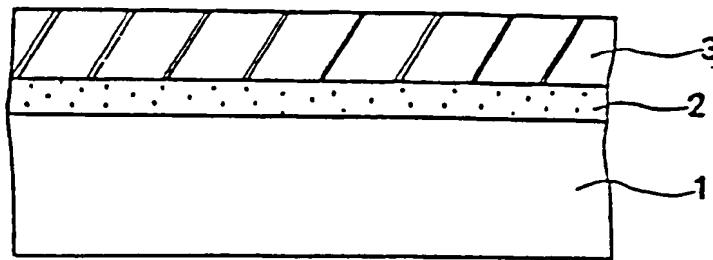


FIG. 1

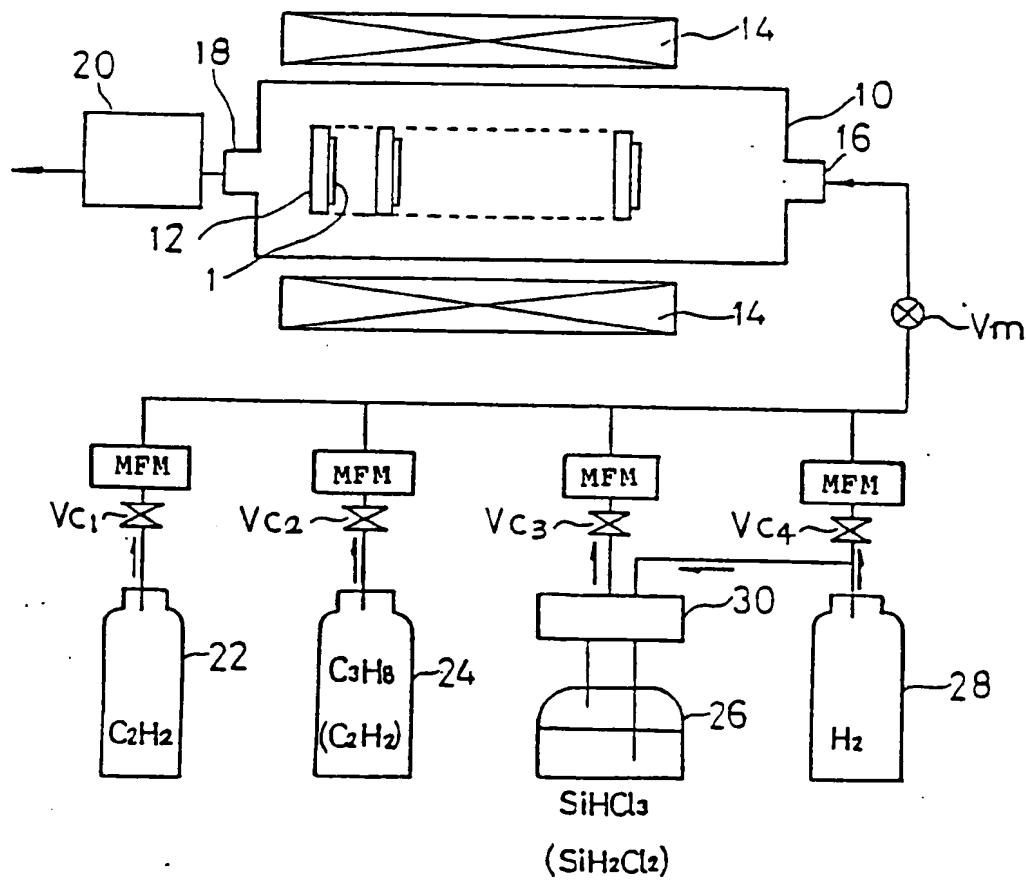


FIG. 2

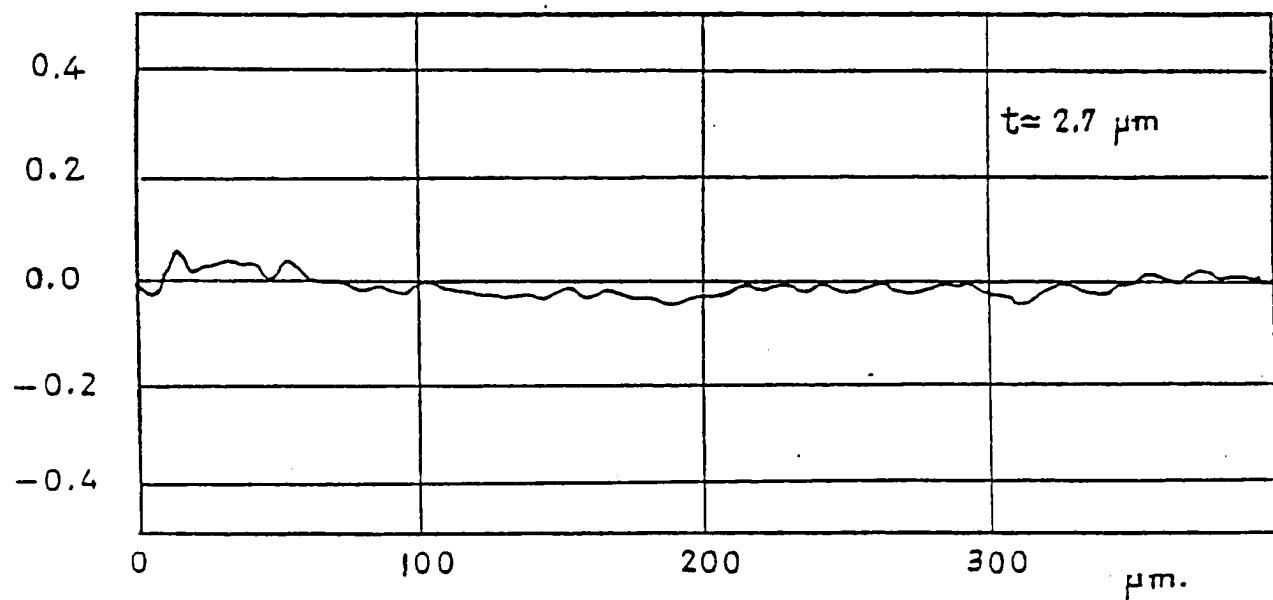
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FIG. 3(a)

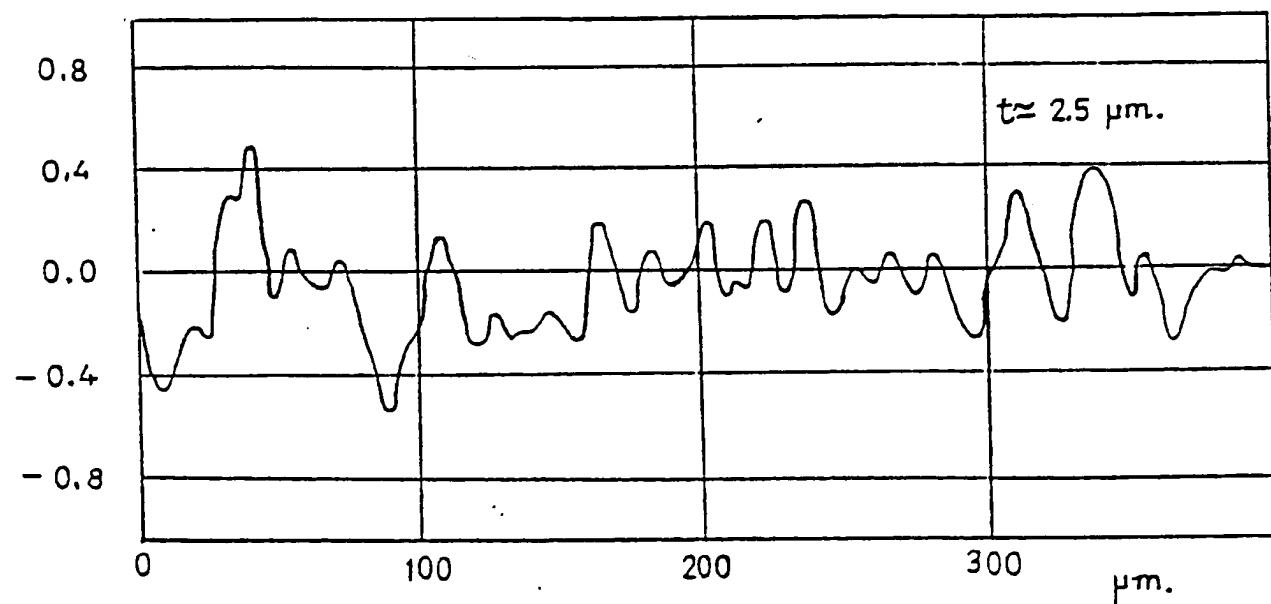
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FIG. 3(b)

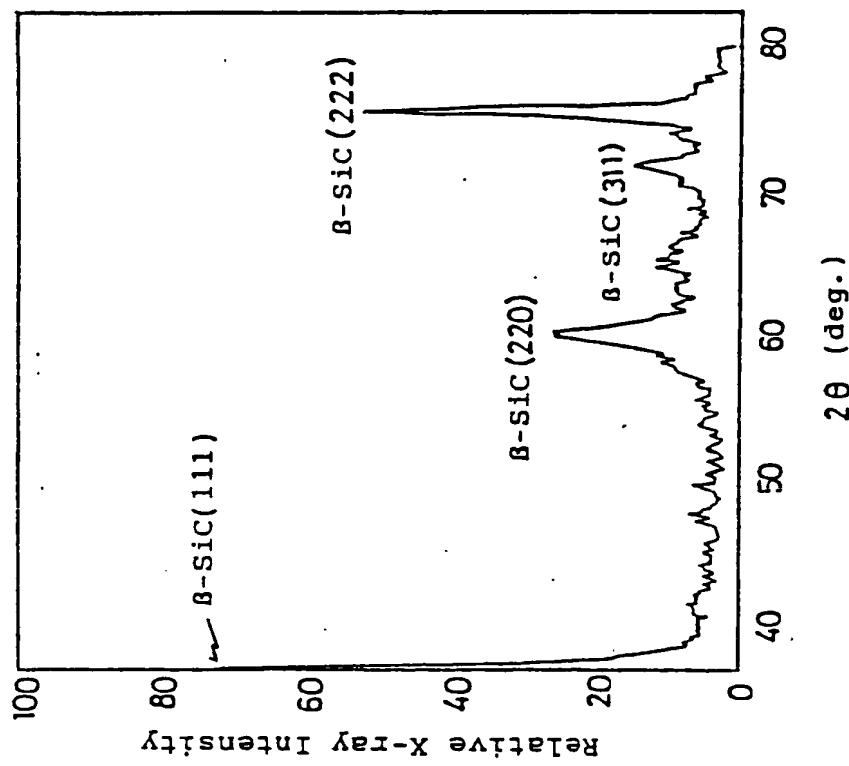


FIG. 4(a)

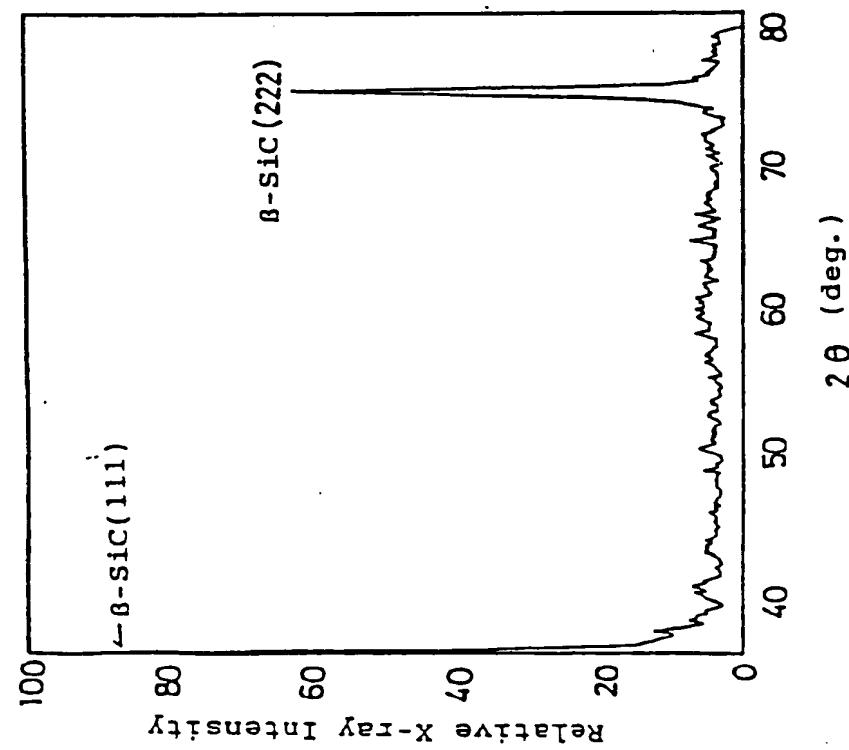
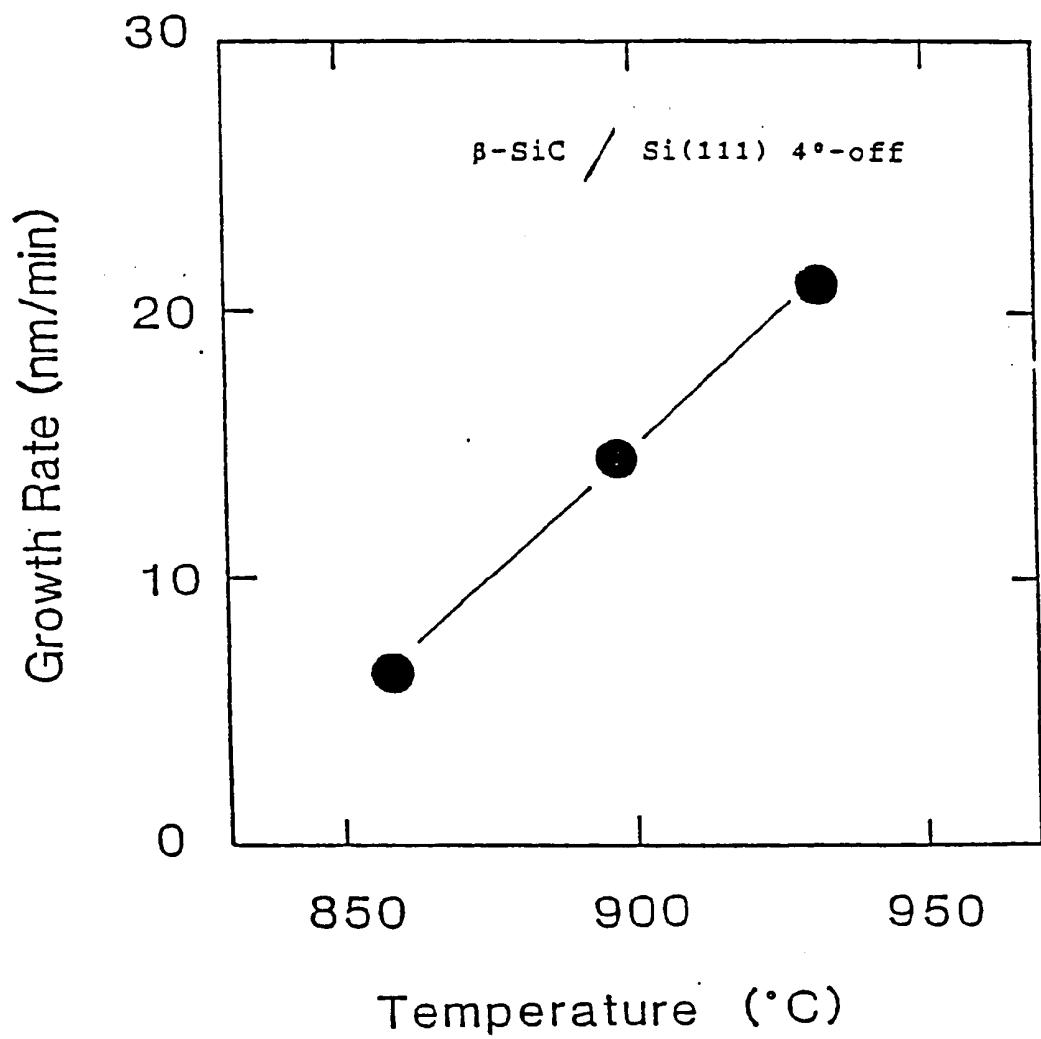


FIG. 4(b)



*F/G. 5*



European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number

EP 88 12 0580

DOCUMENTS CONSIDERED TO BE RELEVANT		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 4)
Category	Citation of document with indication, where appropriate, of relevant passages		
P, Y	PATENT ABSTRACTS OF JAPAN, vol. 12, no. 342 (E-658)[3189], 14th September 1988; & JP-A-63 102 311 (SANYO ELECTRIC CO. LTD) 07-05-1988 * Whole abstract * ---	1-7	C 30 B 25/02 C 30 B 29/36 H 01 L 21/365
P, Y D	JOURNAL OF THE ELECTROCHEMICAL SOCIETY, vol. 135, no. 5, May 1988, pages 1255-1260, Manchester, NH, US; Y. FURUMURA et al.: "Heteroepitaxial beta-SiC on Si" * Page 1255 * ---	1-7	
A	JOURNAL OF APPLIED PHYSICS, vol. 45, no. 3, March 1974, pages 1075-1084, American Institute of Physics; C.J. MOGAB et al.: "Conversion of Si to epitaxial SiC by reaction with C2H2" * Page 1076 * ---	1	
A	FR-A-1 467 105 (IBM CORP.) * Claims 1-4 * ---	1, 2	TECHNICAL FIELDS SEARCHED (Int. Cl. 4)
A	JOURNAL OF CRYSTAL GROWTH, vol. 70, no. 1/2, December 1984, pages 287-290, Elsevier Science Publishers B.V., Amsterdam, NL; A. SUZUKI et al.: "Epitaxial growth of beta-SiC single crystals by successive two-step CVD" * Page 287 * ---	1-3	C 30 B H 01 L
The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	22-03-1989	COOK S.D.	
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone	T : theory or principle underlying the invention		
Y : particularly relevant if combined with another document of the same category	E : earlier patent document, but published on, or after the filing date		
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## EUROPEAN SEARCH REPORT

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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)
A	MATERIALS LETTERS, vol. 4, no. 2, February 1986, pages 77-80, Elsevier Science Publishers B.V., Amsterdam, NL; G.L. HARRIS et al.: "Low-pressure growth of single-crystal silicon carbide" * Page 77 * -----	1,2,12	
TECHNICAL FIELDS SEARCHED (Int. Cl.4)			
The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	22-03-1989	COOK S.D.	
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